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5-12-04
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Pechanek (et al.)

Serial No.: 10/036,789

Filed: December 21, 2001

For: MANIFOLD ARRAY PROCESSOR

Group: 2155

Examiner: Eng, David Y.

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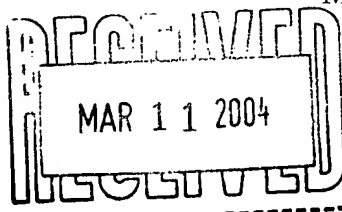
Signed: Marianna Tortorelli

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INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is being filed after the receipt of a first Official Action. Pursuant to 37 C.F.R. 1.56, 1.97 and 1.98, applicant's attorney wishes to bring to the attention of the Patent and Trademark Office the following items cited in the attached European Search Report and listed on the accompanying Form PTO/SB/08A. The undersigned certifies that each item of information contained in this statement was first cited in any communication from a foreign patent office in a counterpart foreign application dated February 18, 2004, which is not more than three months prior to the filing of this statement.

The Commissioner is authorized to charge any fees or credit any overpayments to Law Offices of Peter H. Priest Deposit Account No. 50-1058.

ITEMS

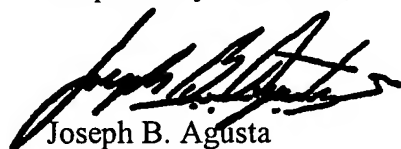
	<u>Publication Date</u>	<u>Patentee/Applicant</u>
1. EP 0569763 A	11//18/1993	International Business Machines Corporation

Other Publications

2. PECHANЕК ET AL., M.F.A.S.T.: A Single Chip Highly Parallel Image Processing Architecture, Proceedings of the International Conference on Image Processing (ICIP), 10/23/1995, Page(s) 69-72, Volume 3, Publisher: IEEE Comp. Soc. Press

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made nor shall it be construed as an admission that the information cited is considered to be material to patentability, nor shall it be construed that no other material information exists.

Respectfully submitted,



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Attorney Docket Number

800.0119

Complete if Known

Application Number

10/036.789

Filing Date

02/09/2004

First Named Inventor

Pechanek et al.

Art Unit

2155

Examiner Name

Eng

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800.0119

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